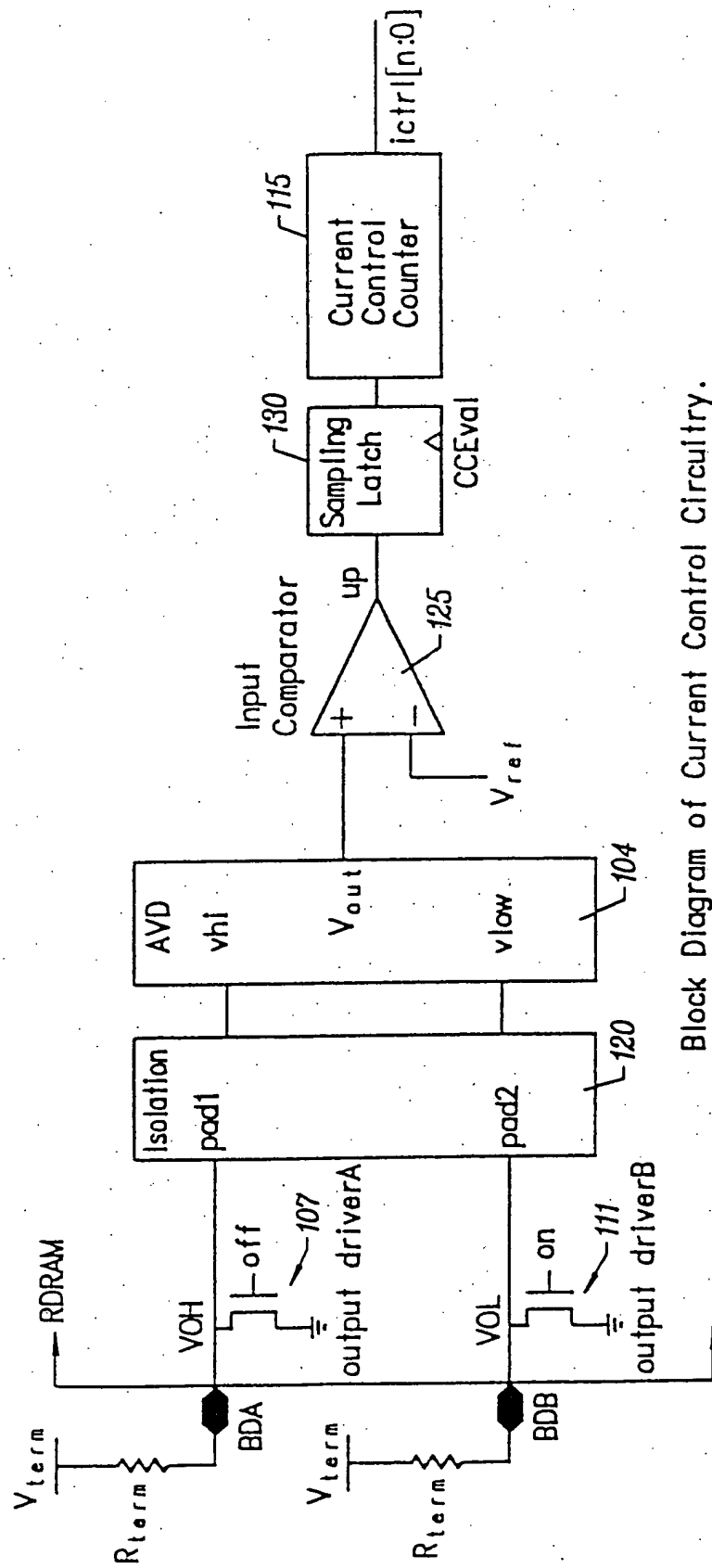


**FIG. 1**  
PRIOR ART



Block Diagram of Current Control Circuitry.

FIG. 2

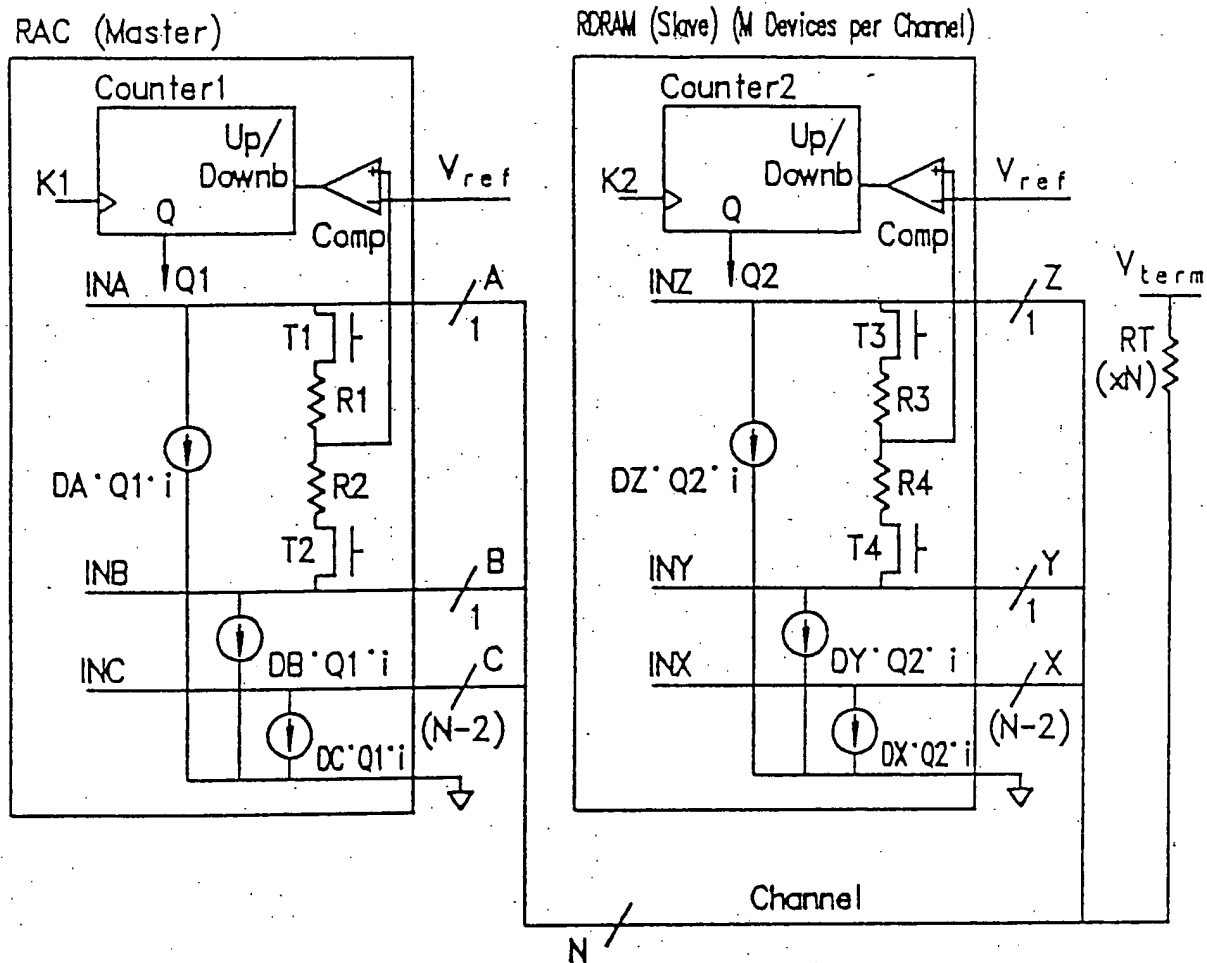


FIG. 3

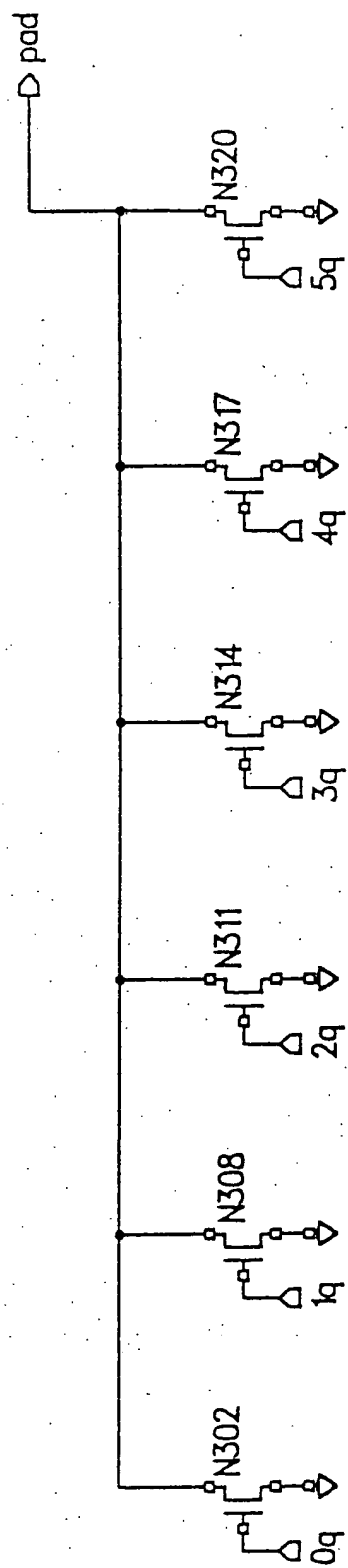


FIG. 4

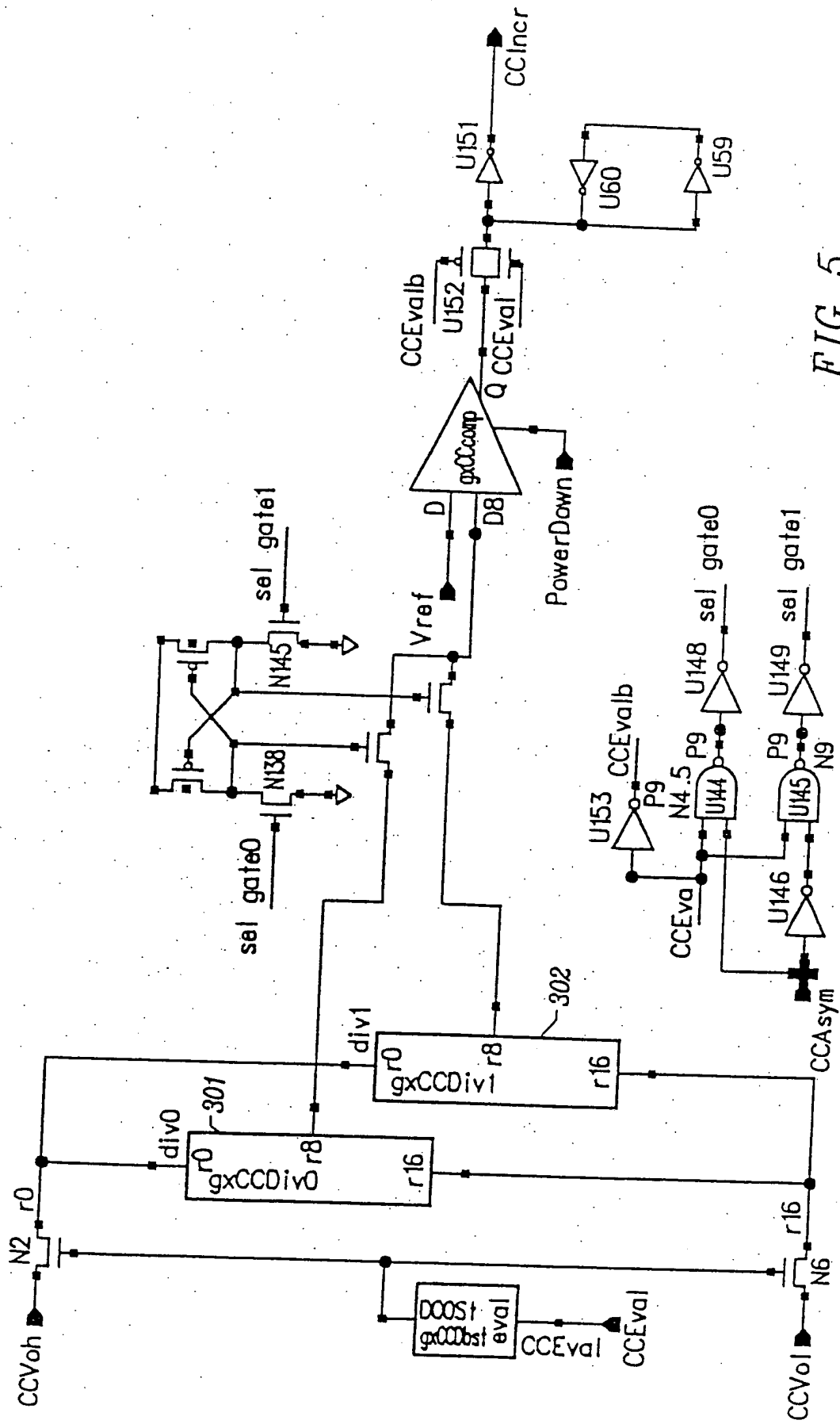


FIG. 5

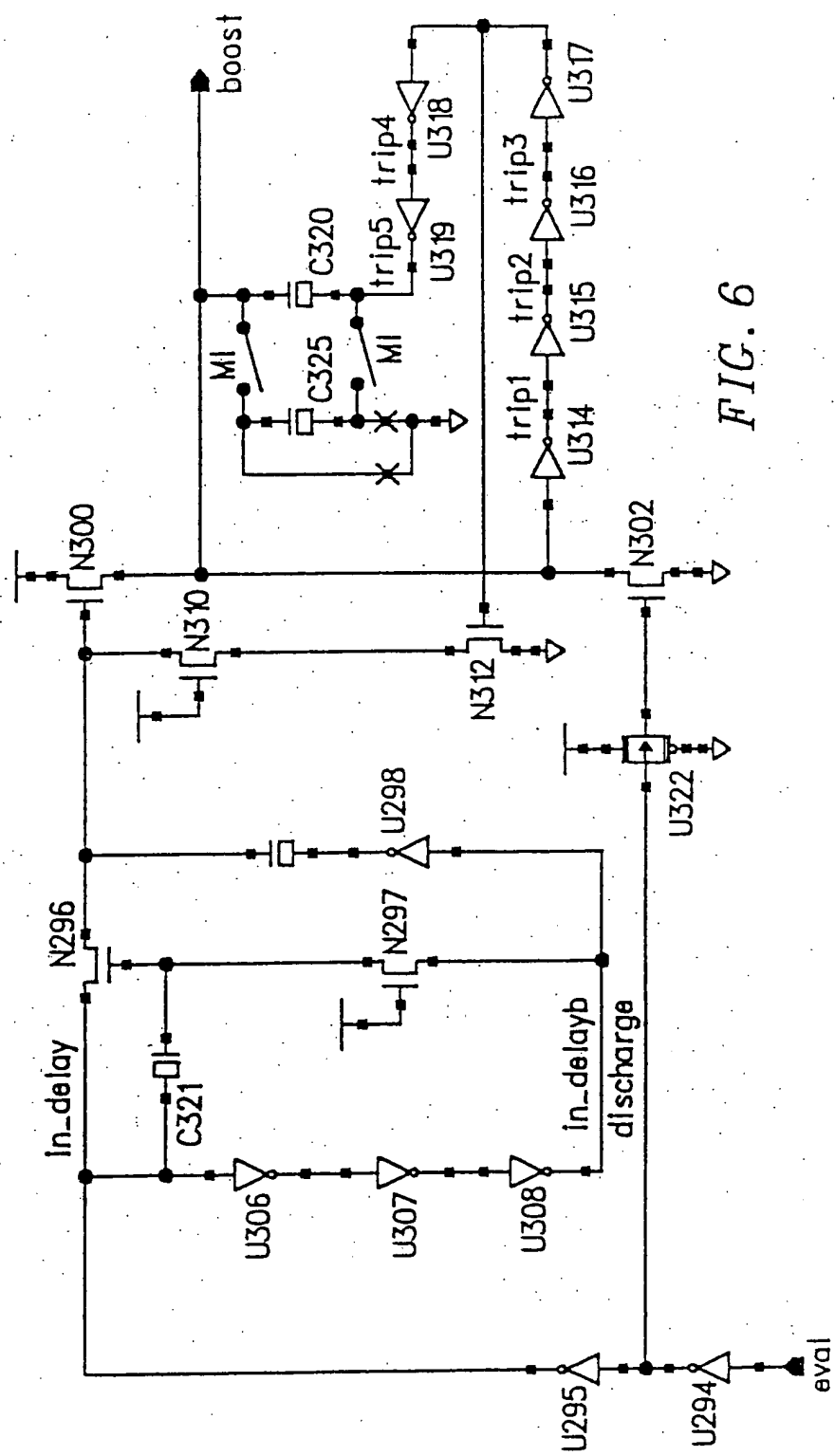


FIG. 6

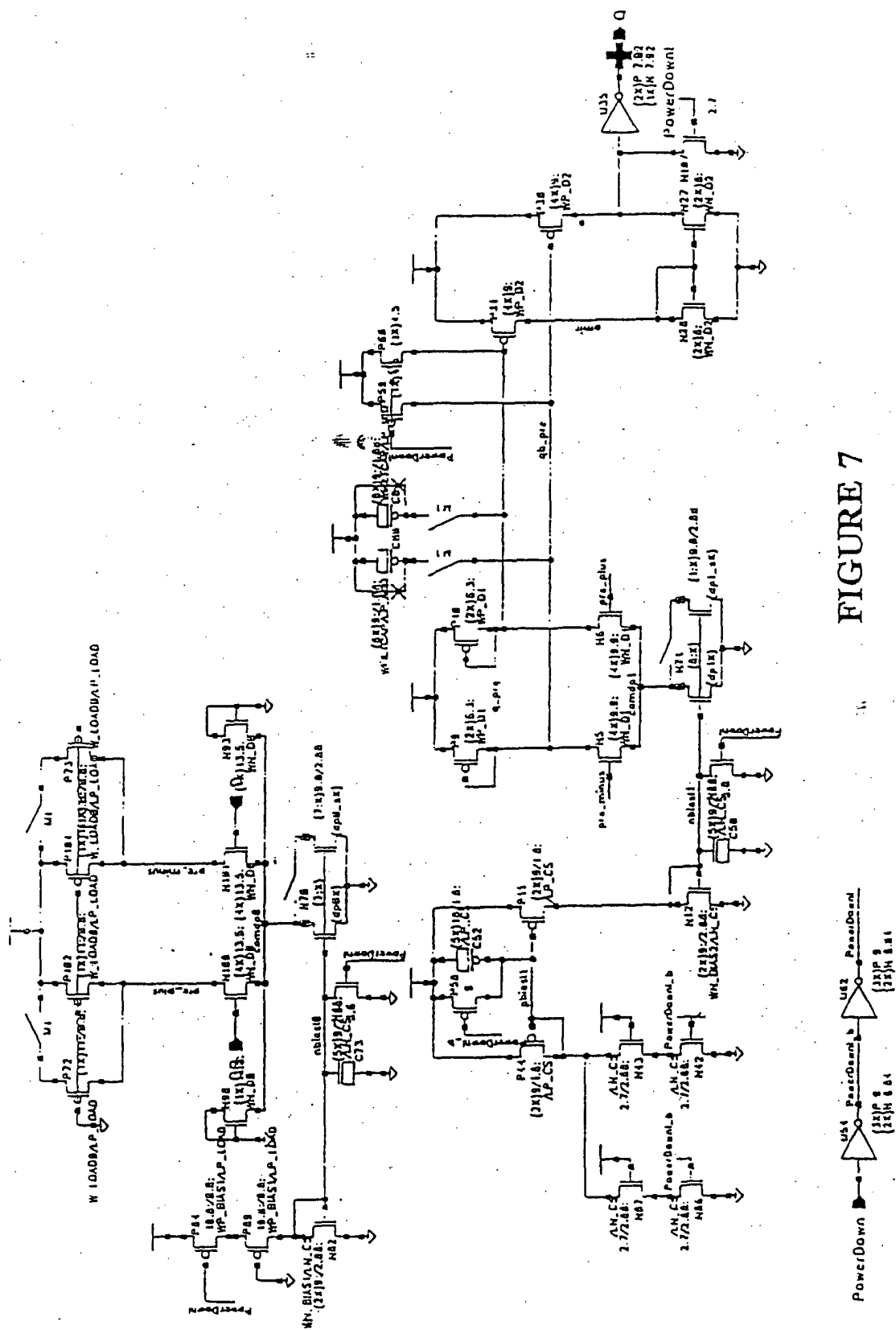




FIG. 8



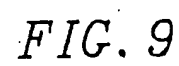
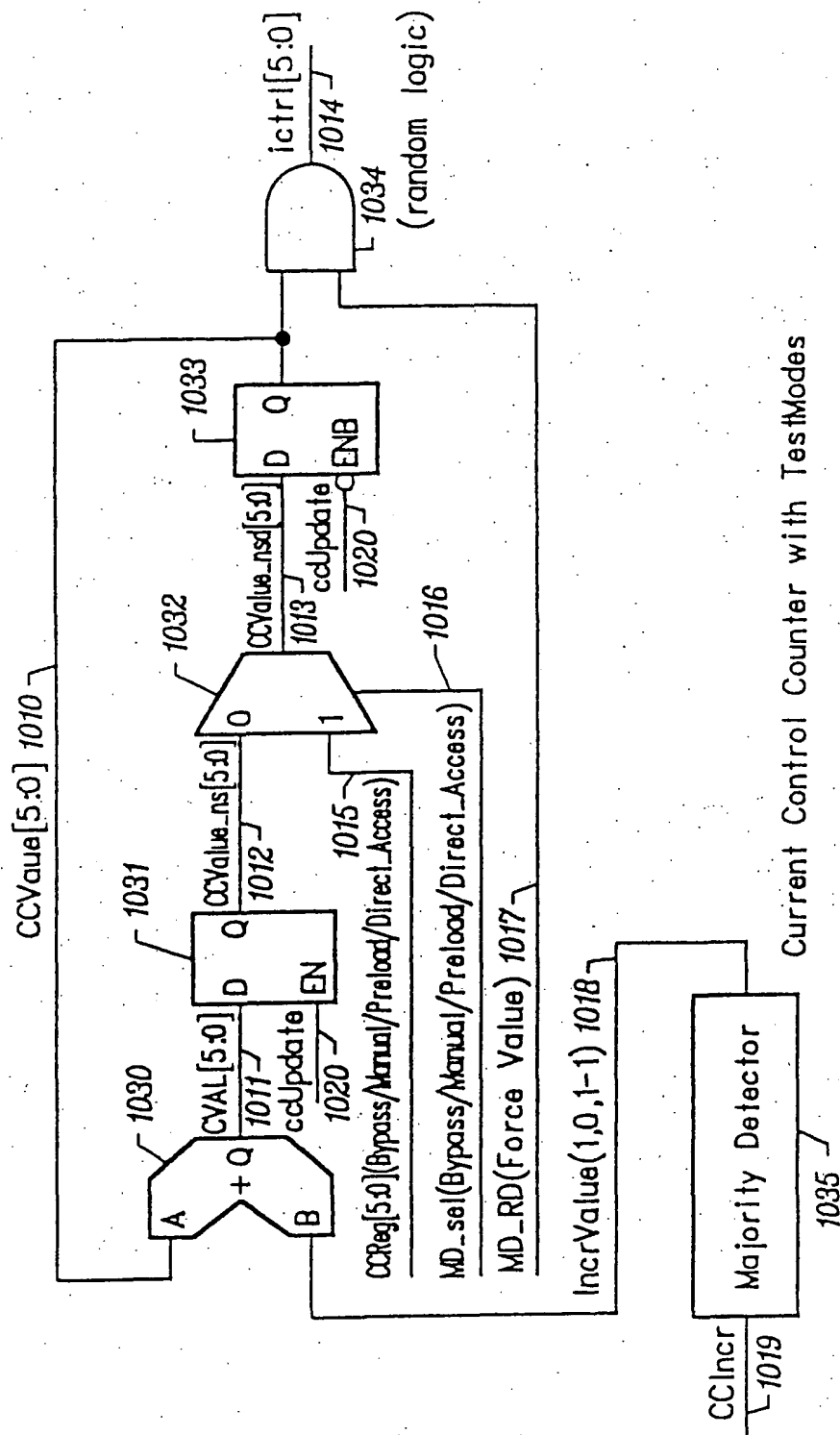


FIG. 9



**FIG. 11**  
Current Control Logic Diagram

